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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/492,568	01/28/2000	Akihiro Ouchi	684.2961	1031

5514 7590 10/21/2003

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EXAMINER

JORGENSEN, LELAND R

ART UNIT	PAPER NUMBER
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2675

DATE MAILED: 10/21/2003

16

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/492,568

Applicant(s)

OUCHI, AKIHIRO

Examiner

Leland R. Jorgensen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 August 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 - 4 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 - 4 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
2. Claims 1 – 4 are rejected under 35 U.S.C. 102(e) as being anticipated by Furuhashi et al., USPN 5,909,205.

Claim 1

Furuhashi teaches a picture display apparatus comprising the following.

Furuhashi teaches a picture display unit [liquid crystal display panel 124]. Furuhashi, col. 6, lines 63 –67 and figure 1.

Furuhashi teaches picture display unit drive means for converting inputted picture signals [analog video signal 102] into display picture signals [display data 121] and generating drive time signals [timing signals 122]. Furuhashi, col. 18, lines 18 – 36; col. 8, line 67 – col. 9, line 9; and figure 1. The picture display unit includes a picture memory [frame memory 100 and line memory 111] for storing picture signals inputted into the picture memory. Furuhashi, col. 7, lines 30 – 54.

Furuhashi teaches display position control means [memory access reconciling signal 123] for detecting the picture display position on the picture display unit based on the display picture and drive timing signals. Furuhashi, col. 8, lines 28 – 36; col. 11, lines 50 – 65; and figures 1 & 2.

Furuhashi teaches a display position control means [frame/line memory control circuit

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.112] for controlling a timing of admission of the inputted picture signals to the picture memory based on the detected display position data from the display position detection means, thereby adjusting a picture display position. Furuhashi, col. 7, lines 55 – 65 and figure 1.

Claim 2

Furuhashi teaches a horizontal synchronizing signal, a vertical synchronizing signal, and a pixel clock signal. Furuhashi, col. 12, line 21 – 24; and figures 5 – 7.

Claim 3

Although Furuhashi does not specifically teach detection of the horizontal and vertical commencement and termination positions, it is inherent to any detection of the pulses shown in figures 5 – 7 that the display position detection means detects such positions. Furuhashi, col. 12, line 21 – 24; and figures 5 – 7. See also Input Horizontal Synchronous Signal Synchronizing Circuit 209 and Internal Horizontal Synchronous Signal Synchronizing Circuit 209, figure 2. Furuhashi teaches display position control means [frame/line memory control circuit 112] that controls a timing of admitting the inputted picture signals into the picture memory in the picture display unit drive means, based on a difference between detected position data and set timing data for outputting display picture signals, thereby automatically adjusting a picture display position. Furuhashi, col. 7, lines 55 – 65 and figure 1.

Claim 4

Furuhashi teaches a preset memory [memory architecture decode circuit 205] for storing and judging the formatting values. Furuhashi, col. 9, line 59 – col. 10, line 8; and figure 1.

Response to Arguments

3. Applicant's arguments filed 12 August 2003 have been fully considered but they are not persuasive.

Applicant argues that Furuhashi fails to teach display position detection means or display position control means. In the prior action, examiner identified display position control means as Furuhashi memory access reconciling signal 123 [see Furuhashi, figures 1 & 2] and display position control means as Furuhashi frame/line memory control circuit 112. Applicant did not rebut such identification. Furuhashi teaches that the frame/line memory control circuit 112 outputs adjustment signals "one the basis of ... a memory access reconciling signal 123,..." Furuhashi, col. 7, lines 57 – 62. Thus, examiner's position remains unrebutted.

Conclusion

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leland Jorgensen whose telephone number is 703-305-2650. The examiner can normally be reached on Monday through Friday, 7:00 a.m. through 3:30 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Steven J. Saras can be reached on 703-305-9720.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or faxed to:

(703) 872-9306

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office, telephone number (703) 306-0377.

lrj



STEVEN SARAS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600